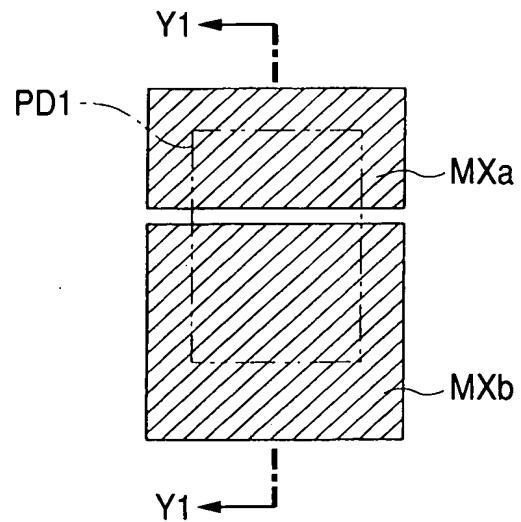
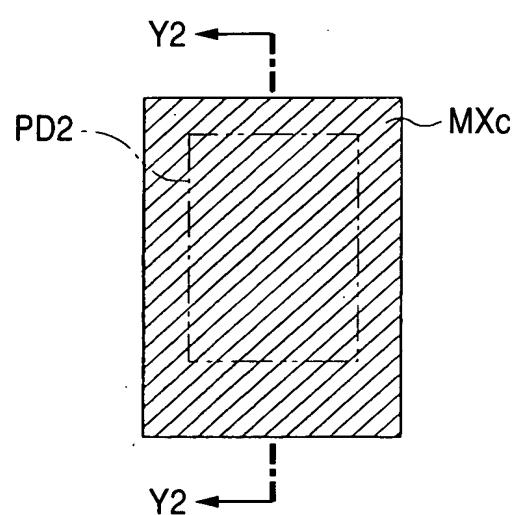
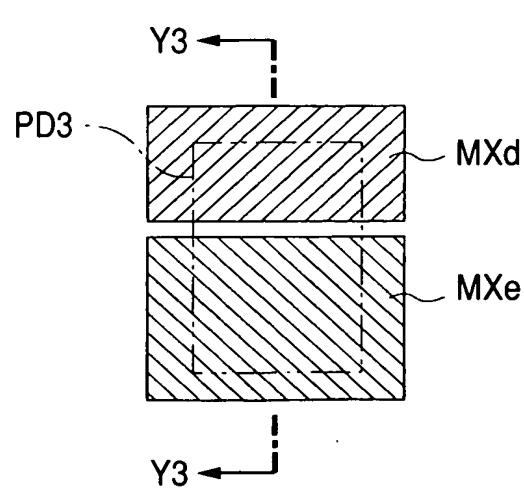
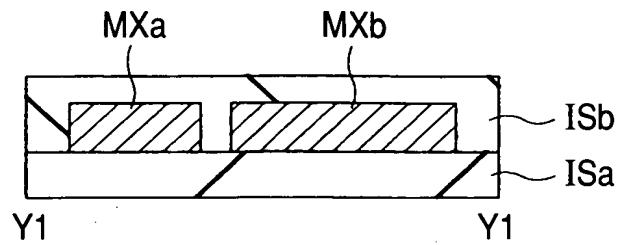
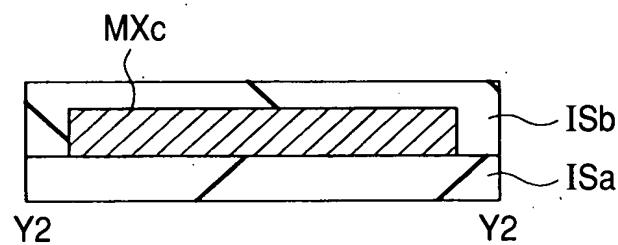


**FIG. 1****FIG. 2****FIG. 3**

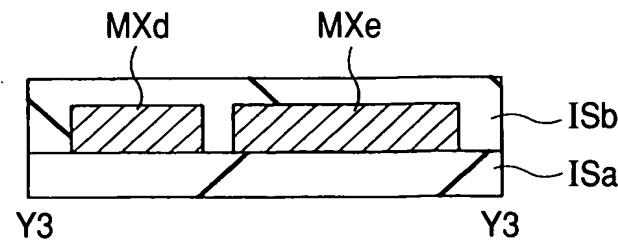
*FIG. 4*



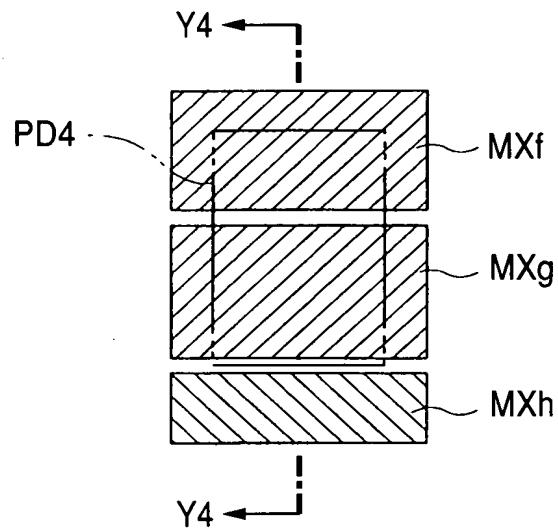
*FIG. 5*



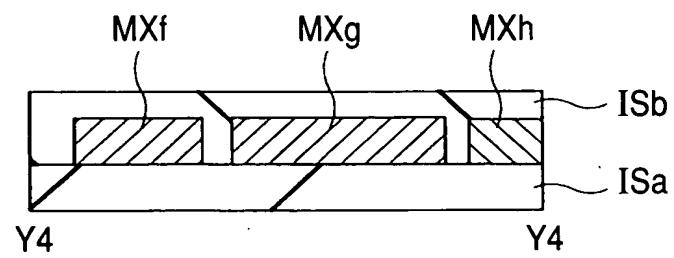
*FIG. 6*

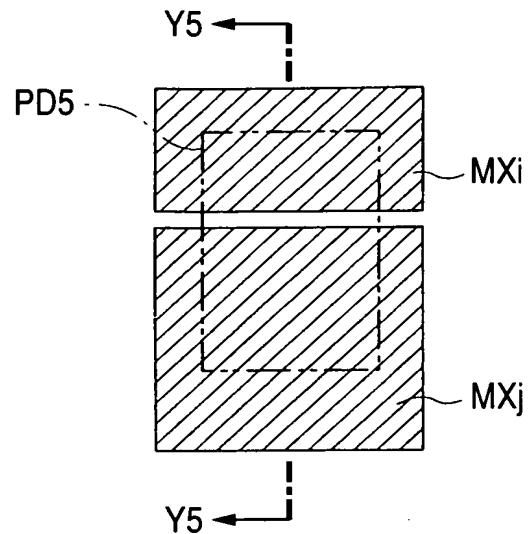
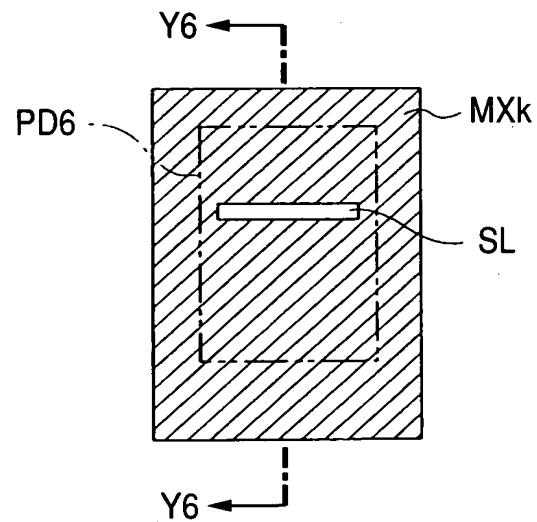
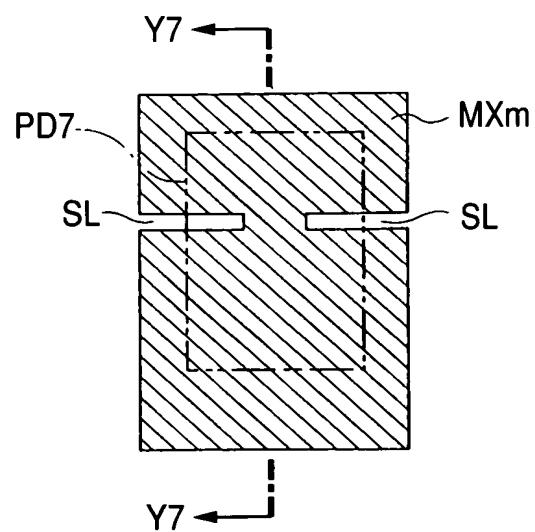


*FIG. 7*

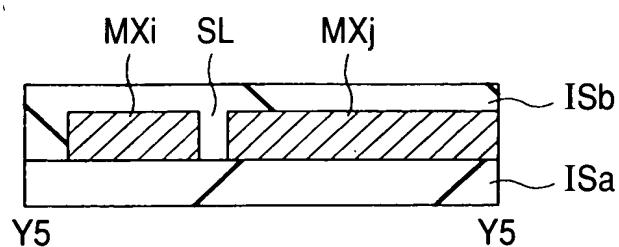


*FIG. 8*

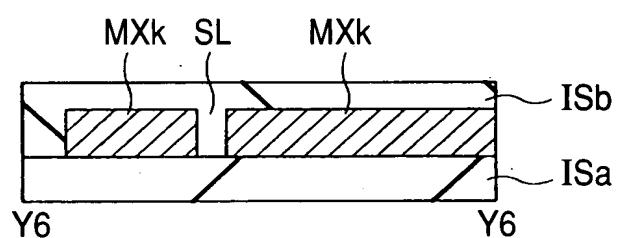


**FIG. 9****FIG. 10****FIG. 11**

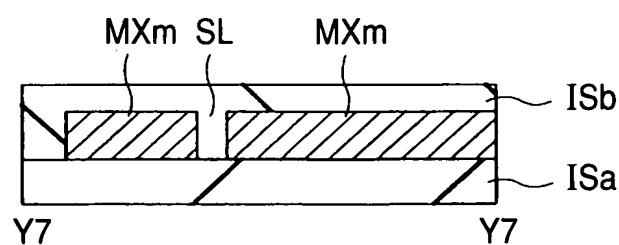
**FIG. 12**



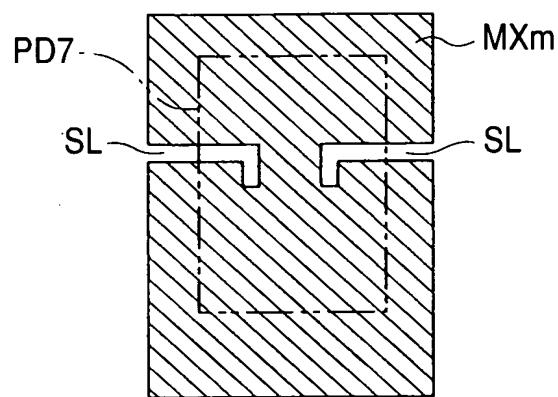
**FIG. 13**



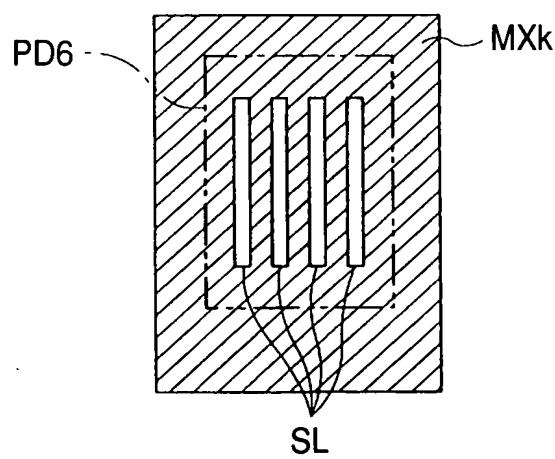
**FIG. 14**



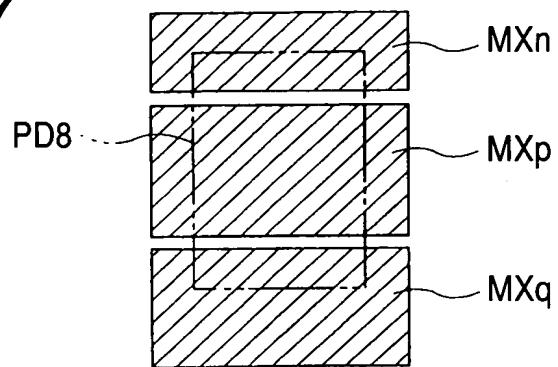
*FIG. 15*



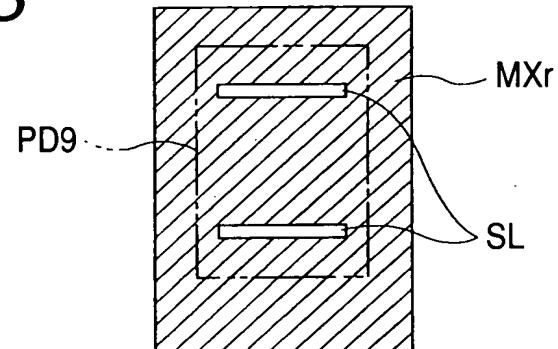
*FIG. 16*



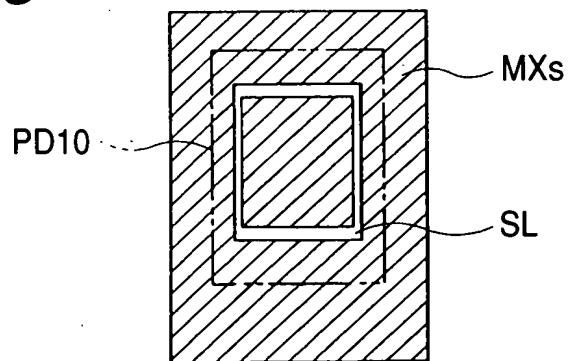
**FIG. 17**



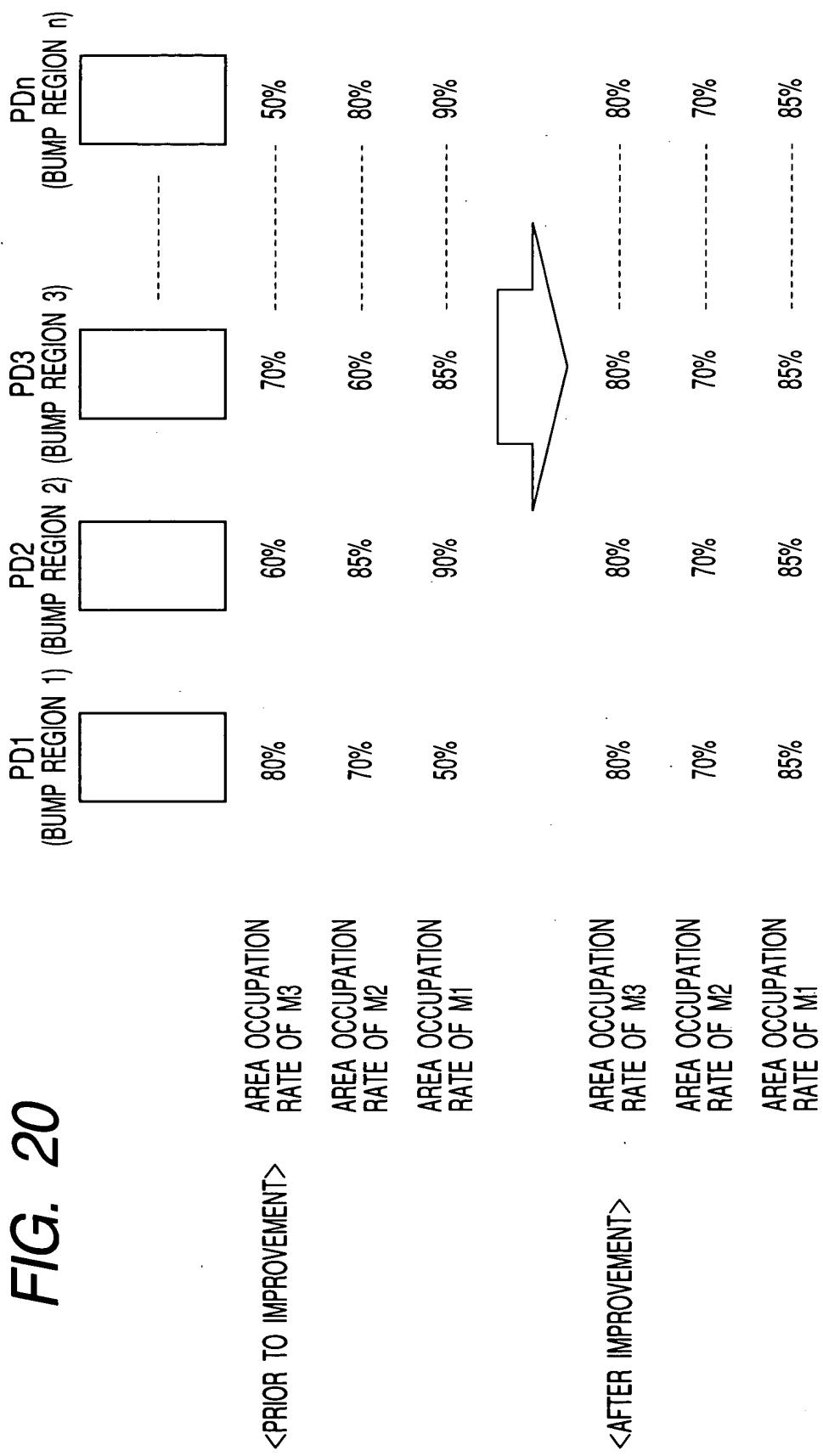
**FIG. 18**



**FIG. 19**

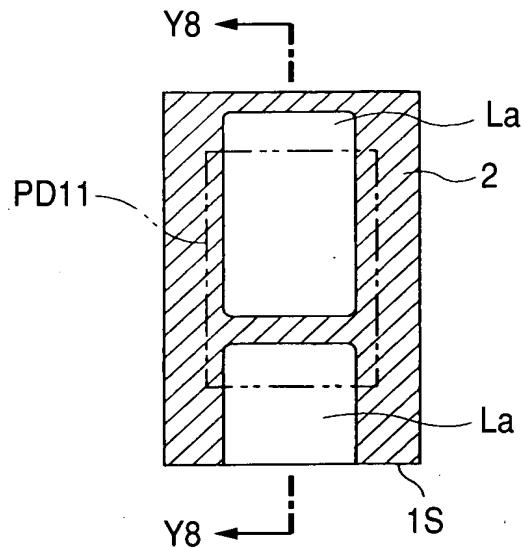


**FIG. 20**

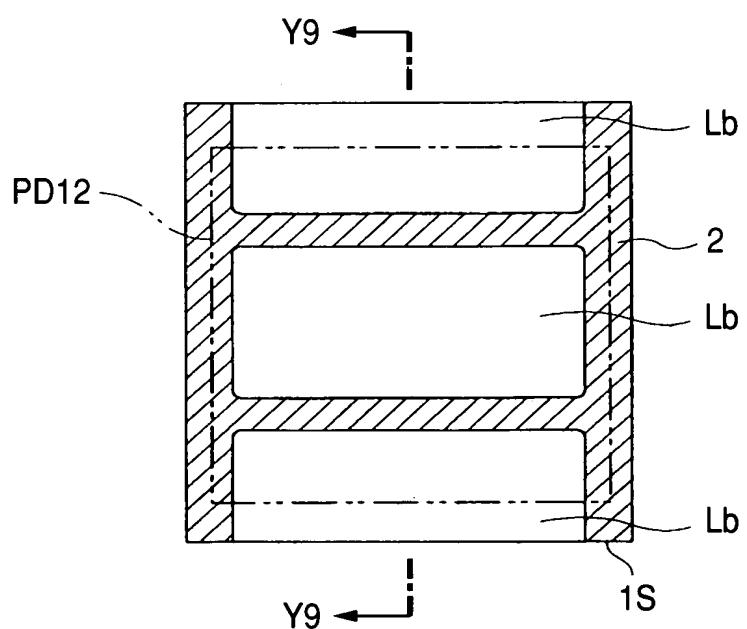


PD1 TO PDn: ELECTRODE PAD  
M1: FIRST-LAYER WIRING  
M2: SECOND-LAYER WIRING  
M3: THIRD-LAYER WIRING

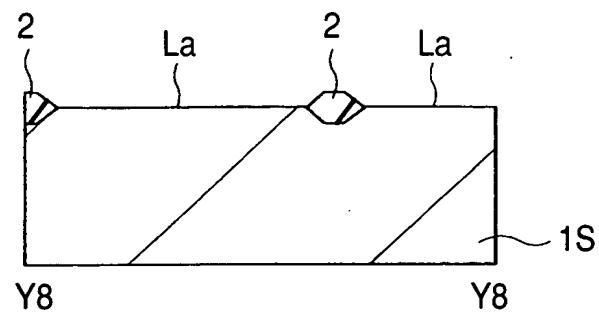
*FIG. 21*



*FIG. 22*



*FIG. 23*



*FIG. 24*

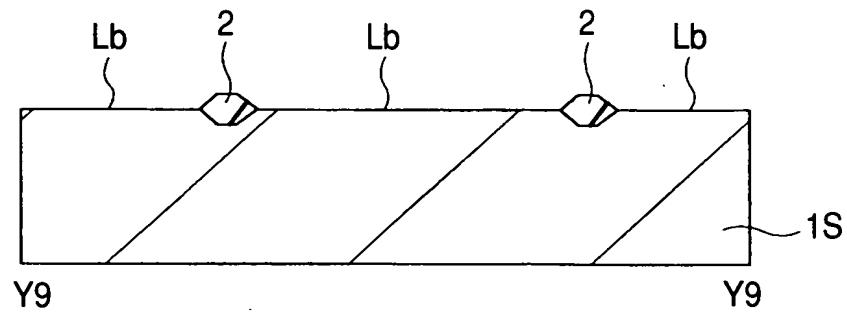
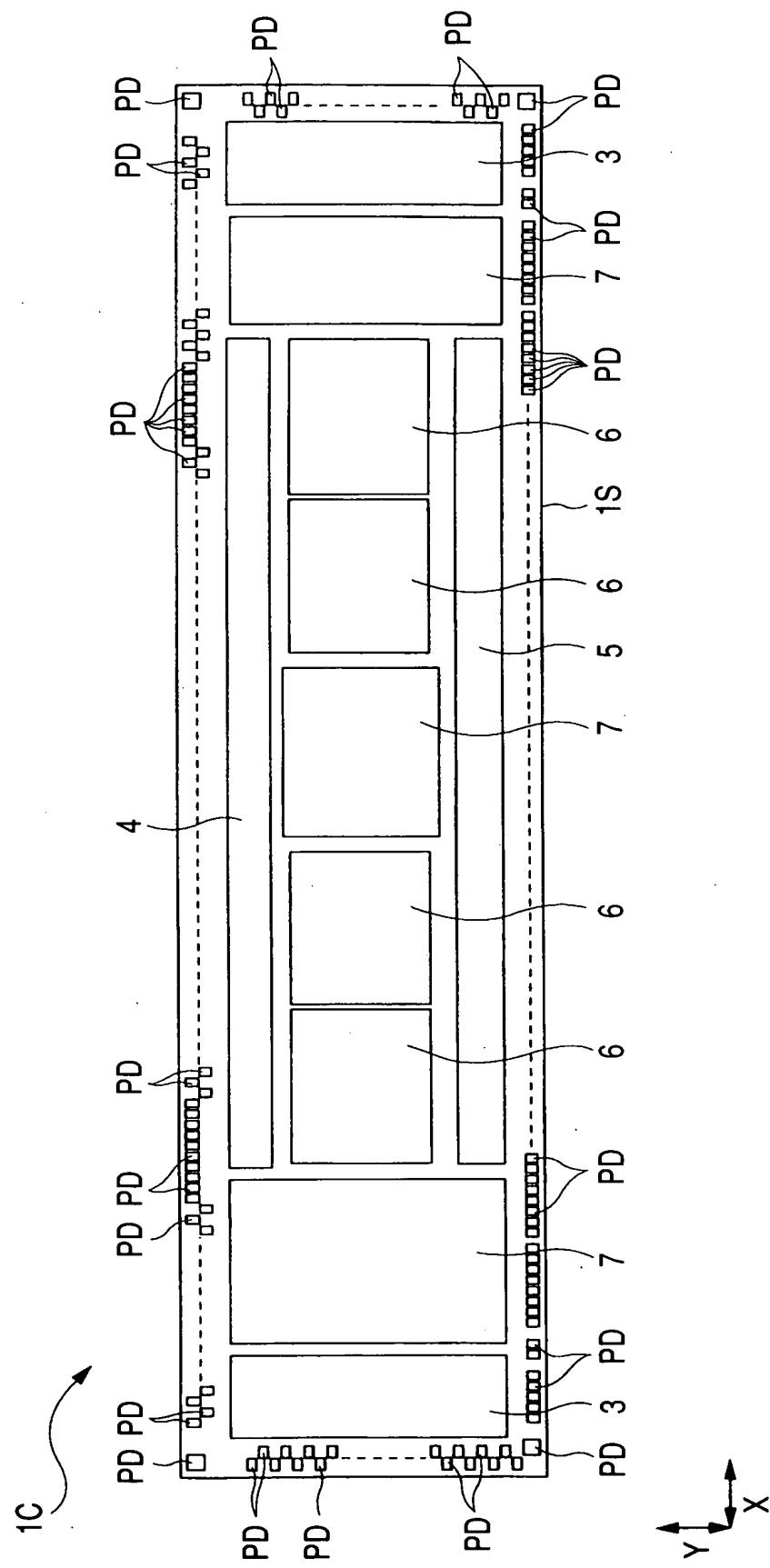
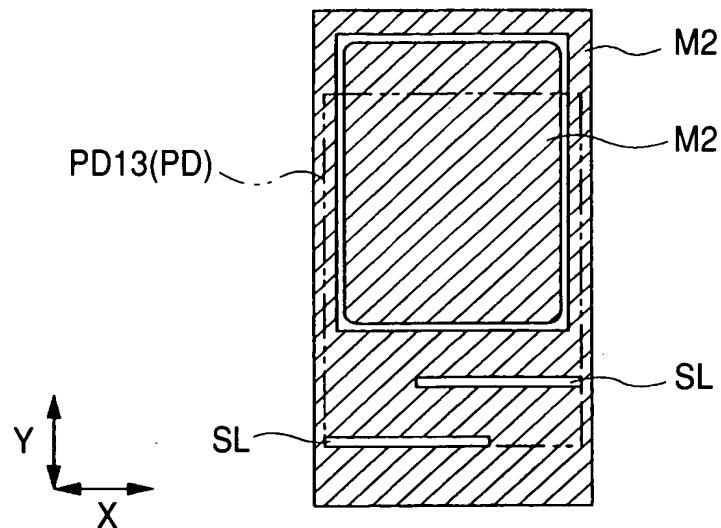


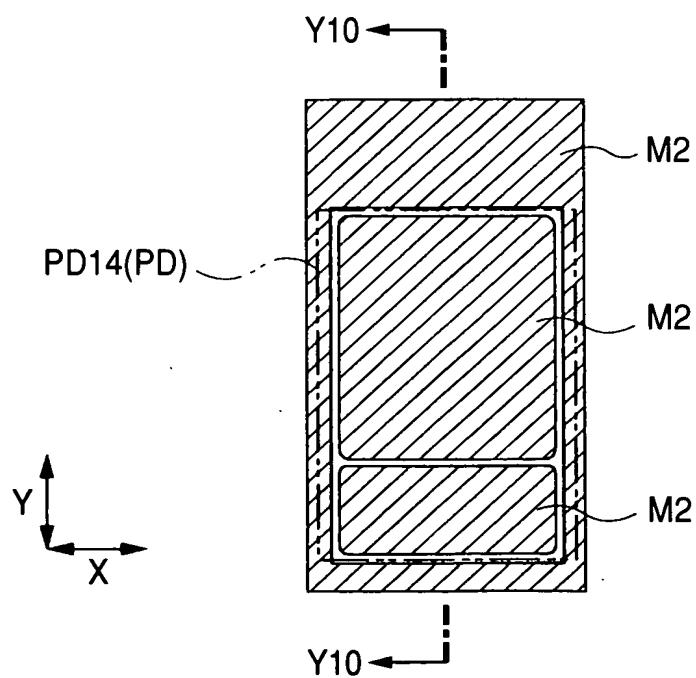
FIG. 25



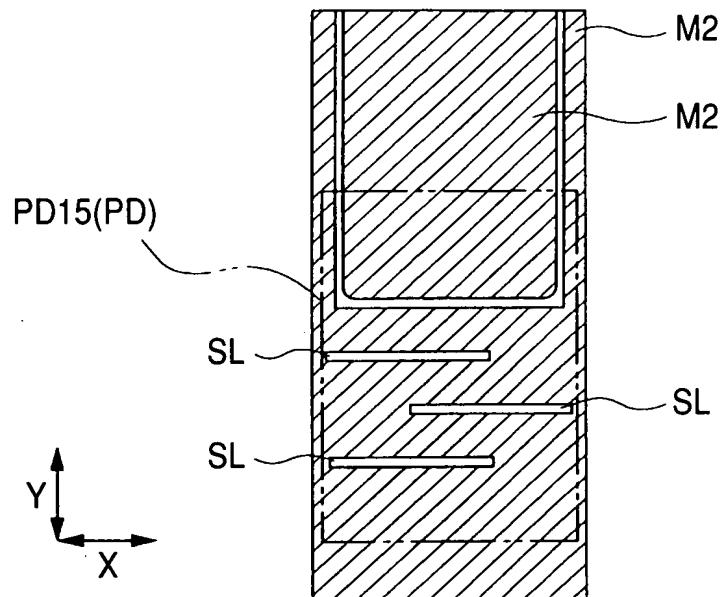
*FIG. 26*



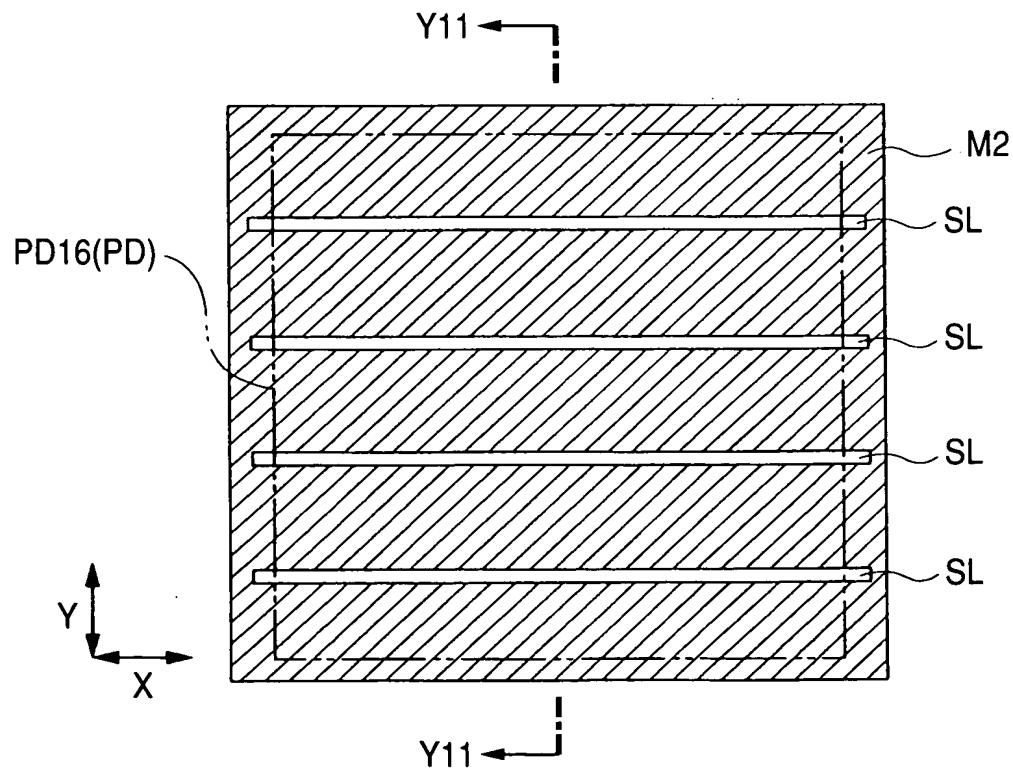
*FIG. 27*



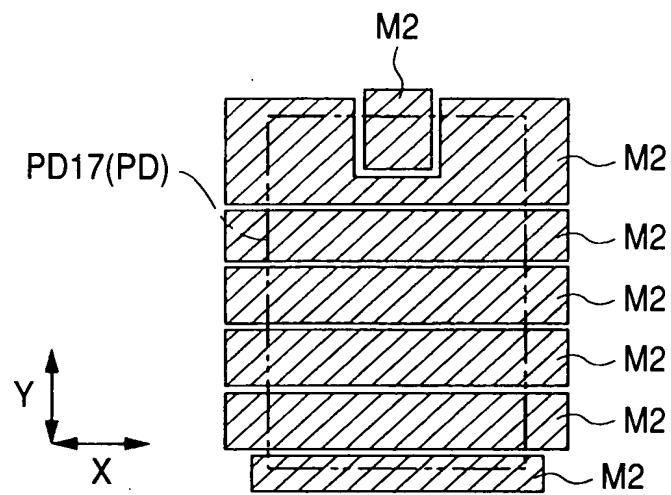
**FIG. 28**



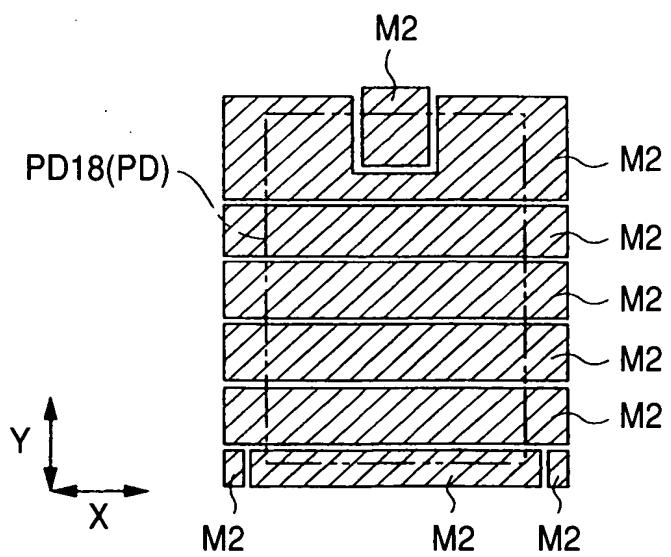
**FIG. 29**



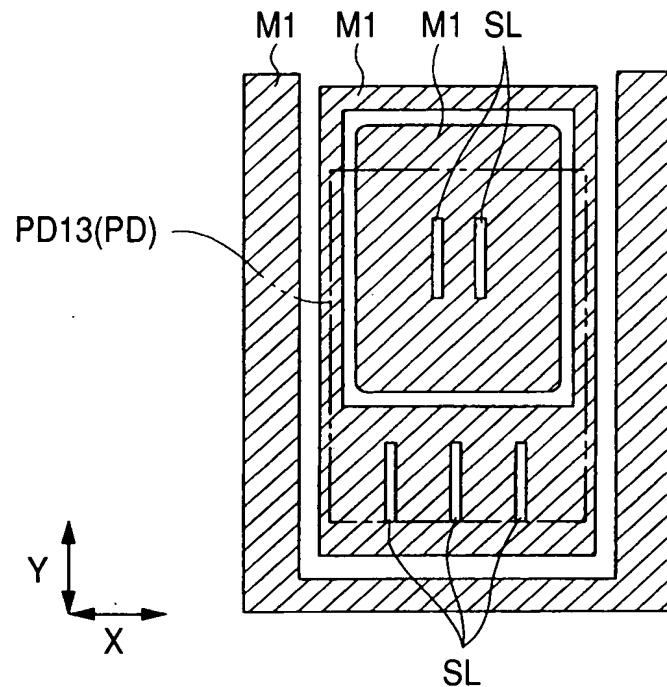
*FIG. 30*



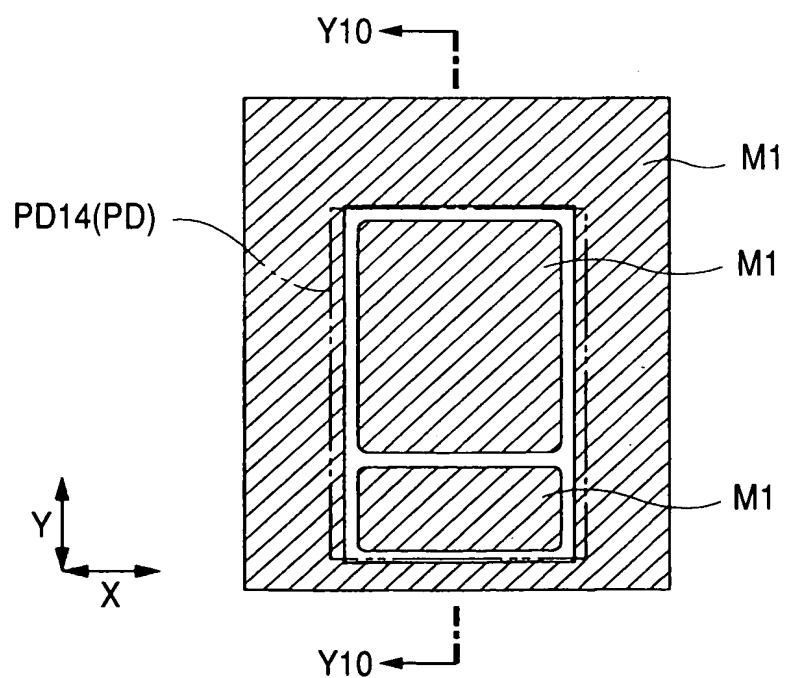
*FIG. 31*

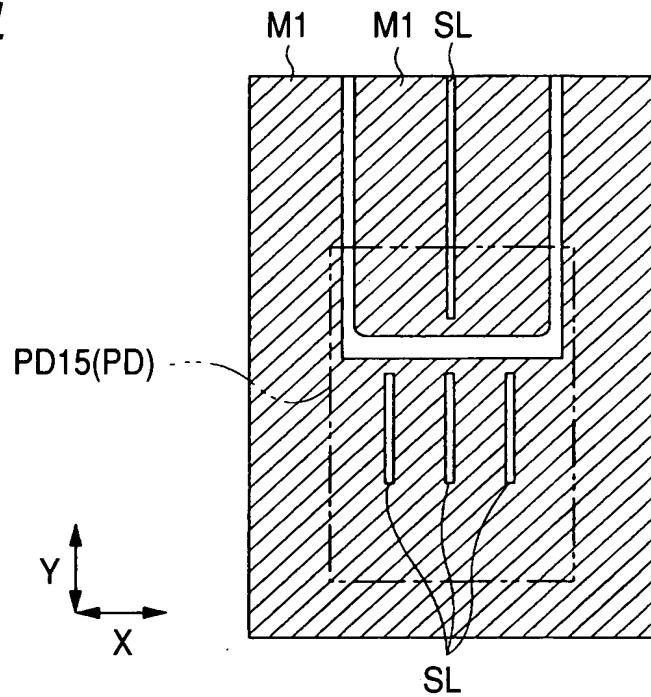
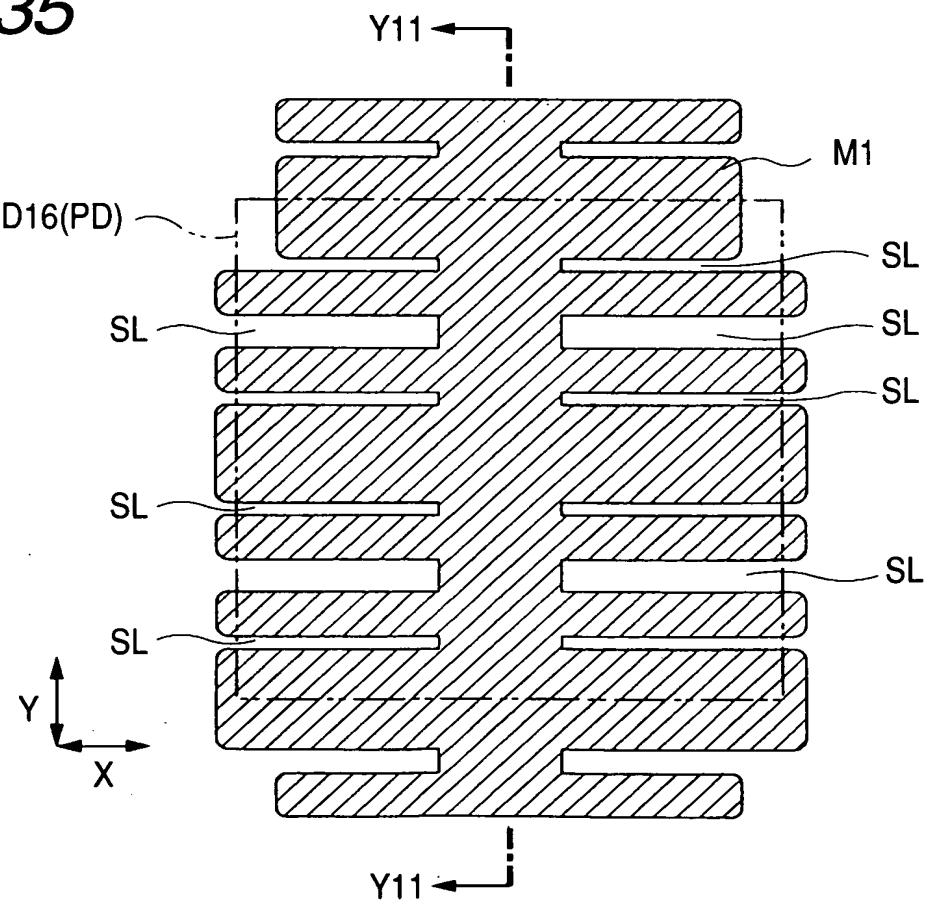


**FIG. 32**

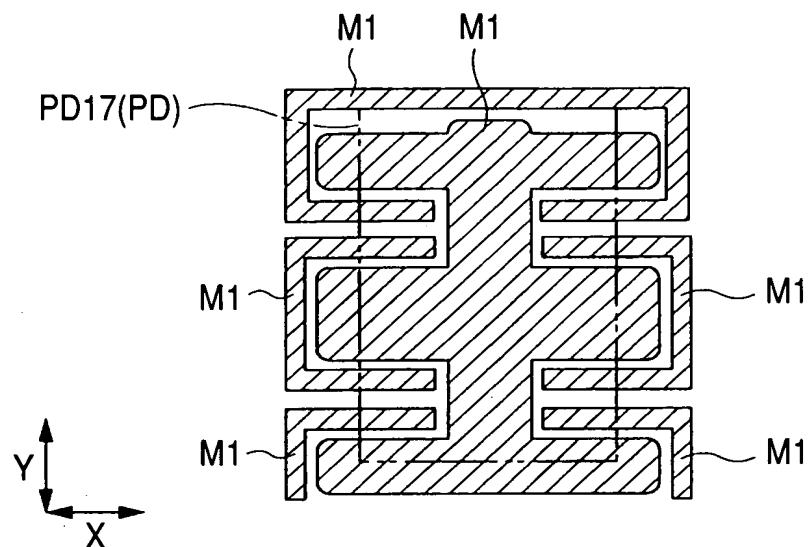


**FIG. 33**

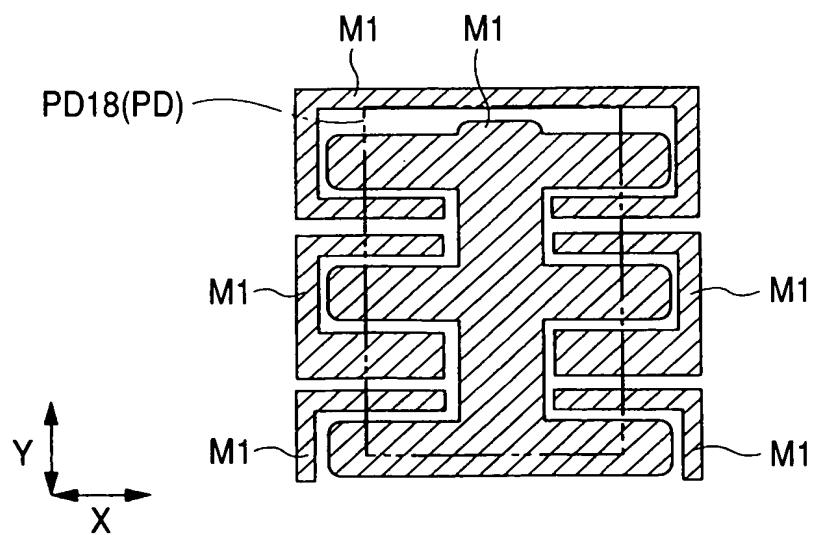


**FIG. 34****FIG. 35**

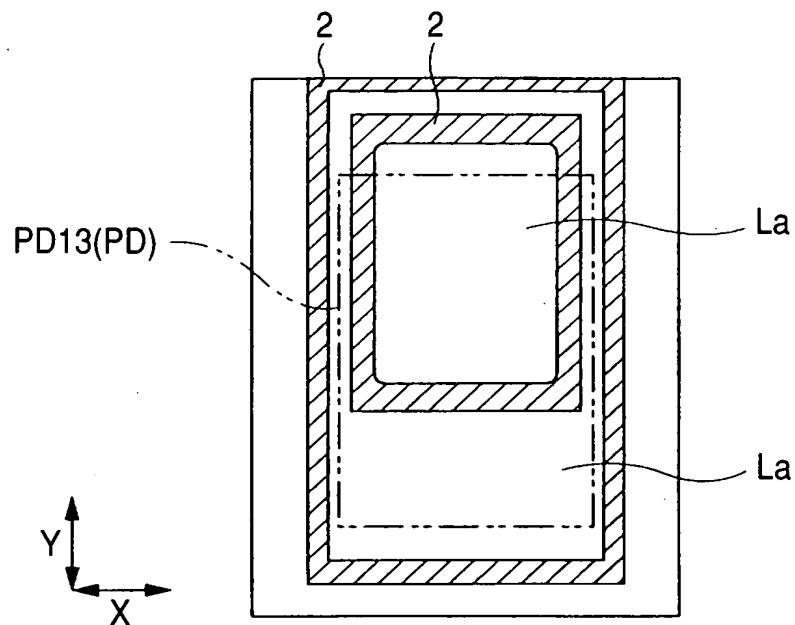
*FIG. 36*



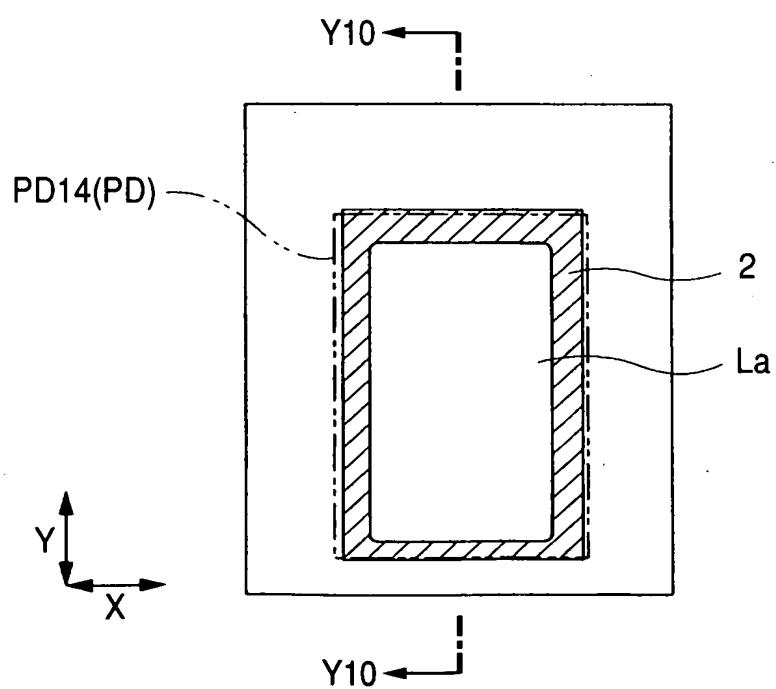
*FIG. 37*

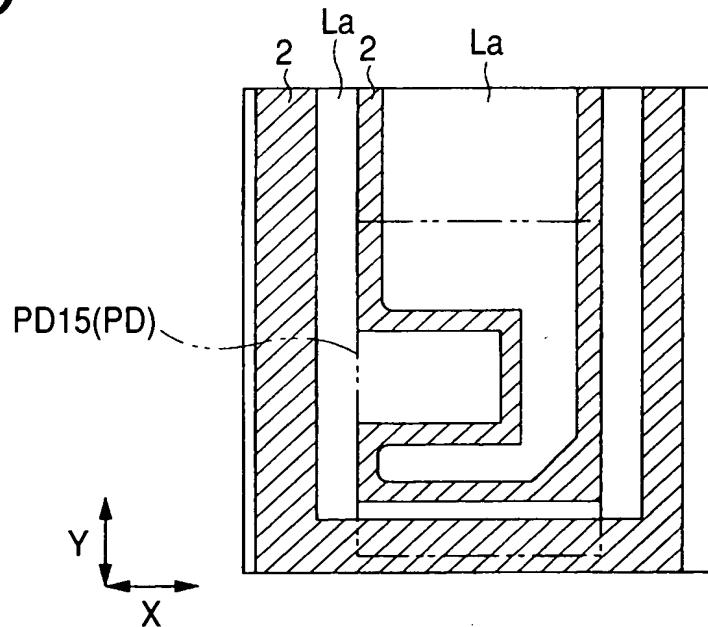
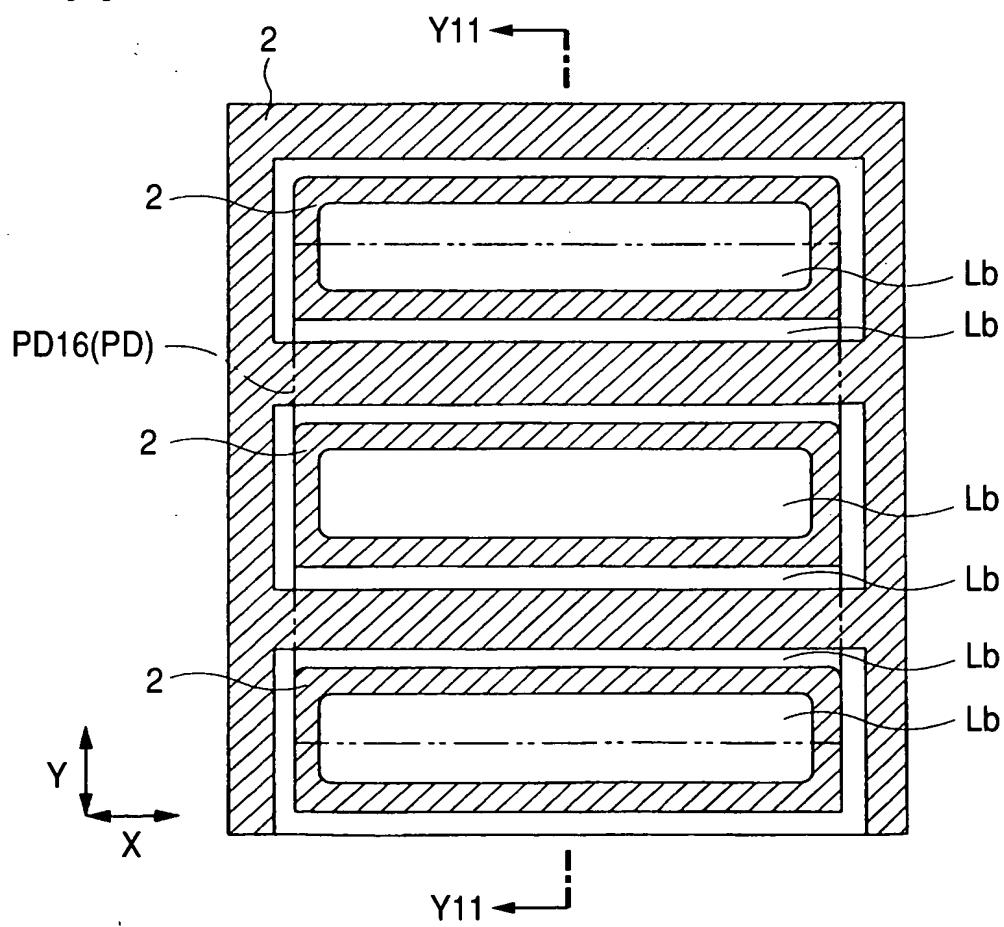


**FIG. 38**

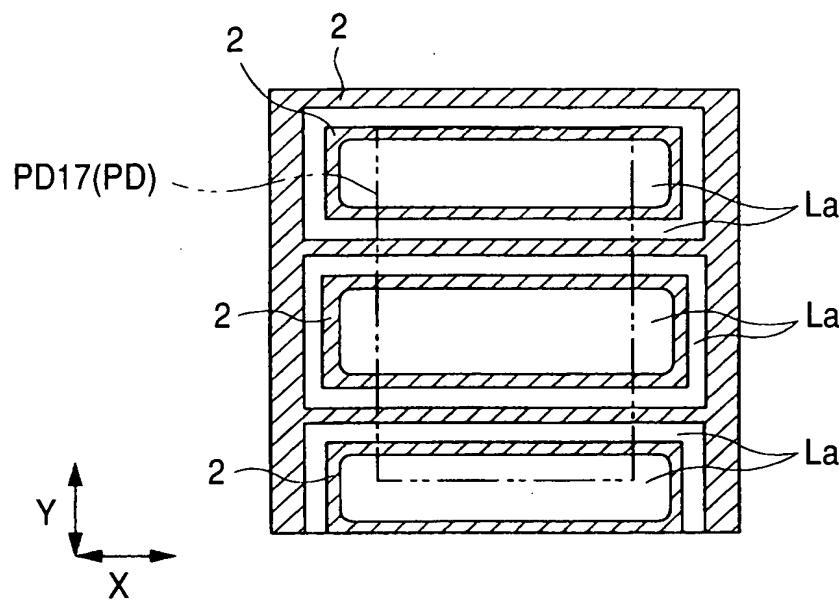


**FIG. 39**



**FIG. 40****FIG. 41**

**FIG. 42**



**FIG. 43**

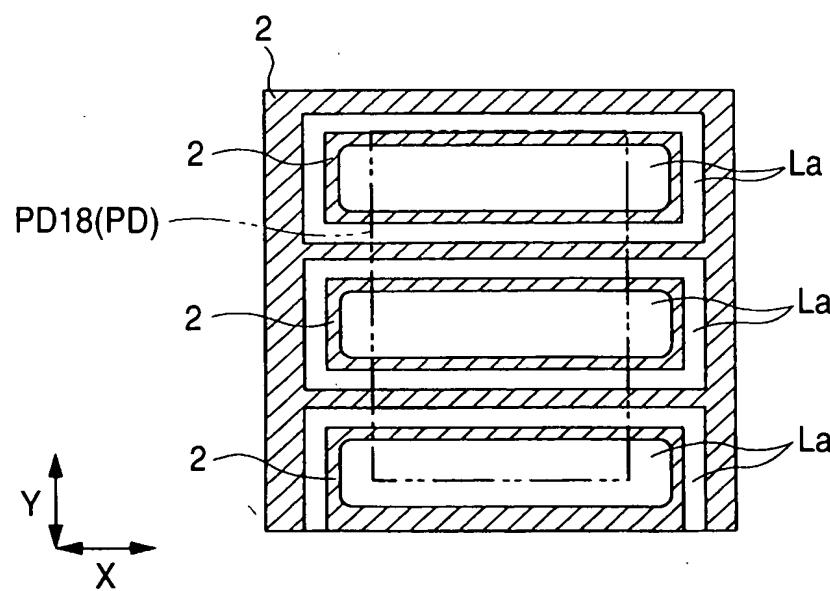


FIG. 44

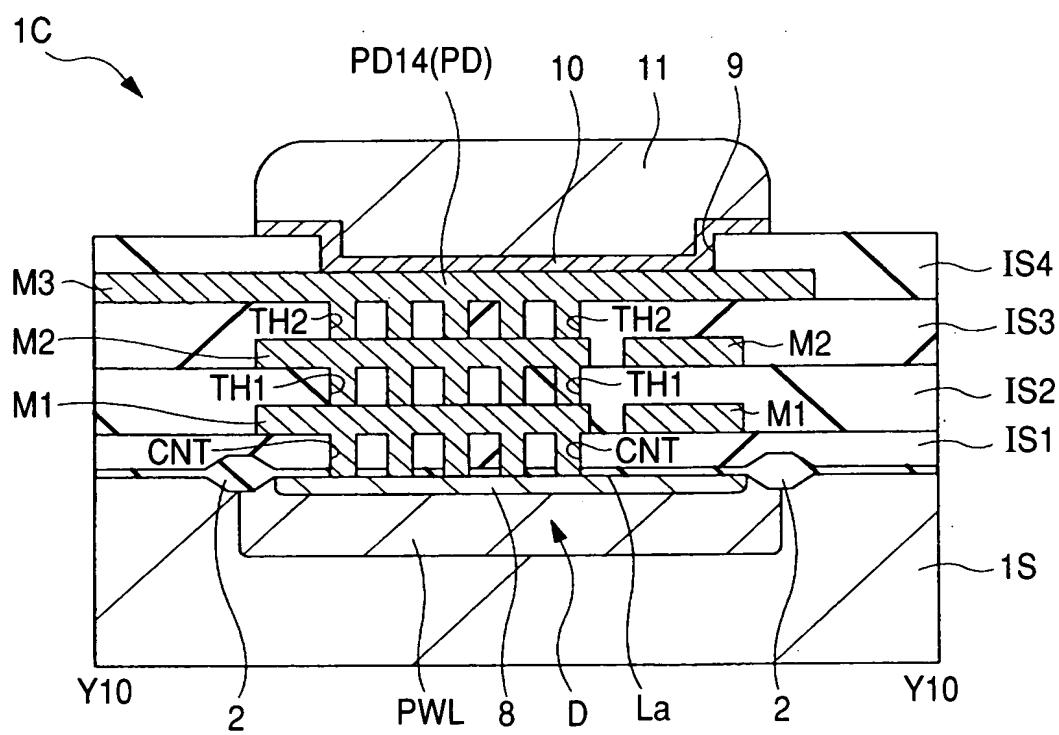
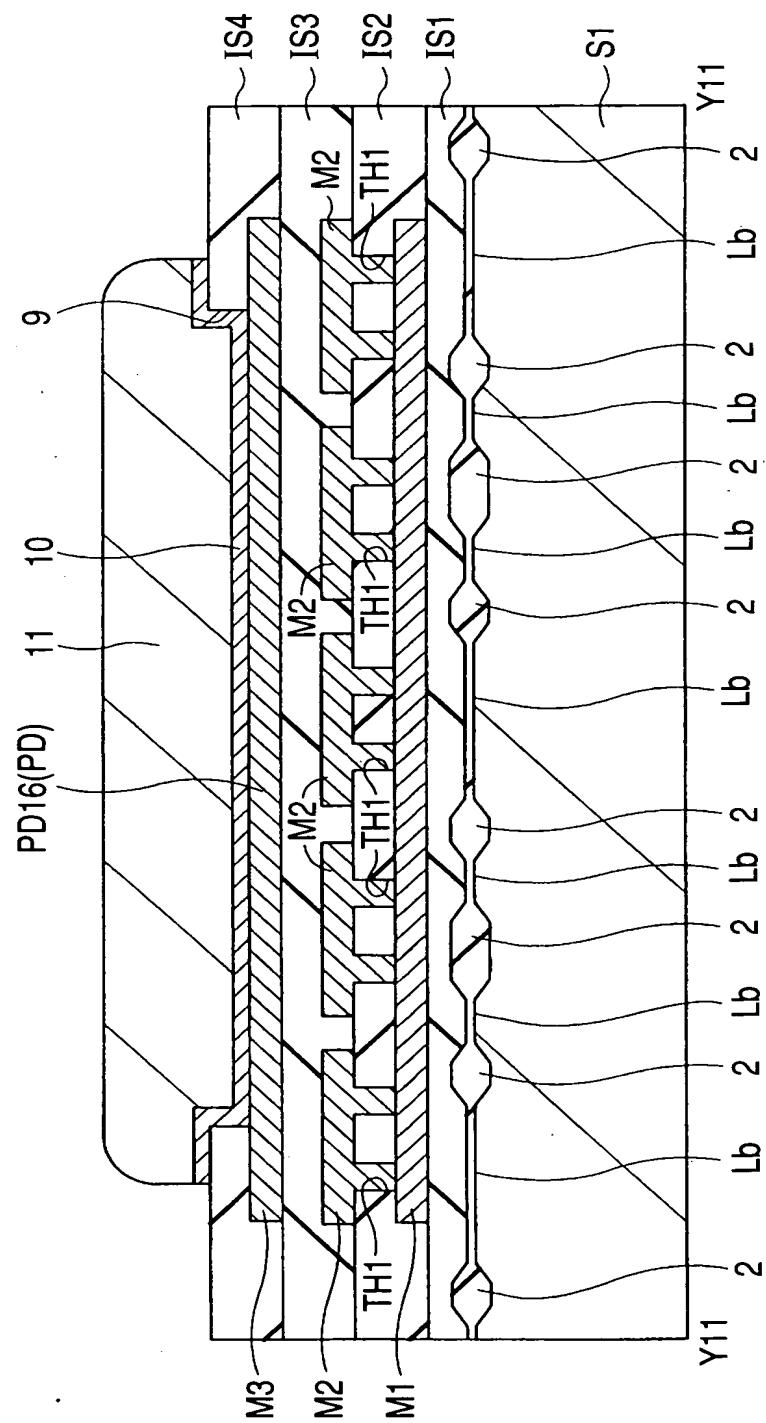


FIG. 45



**FIG. 46**

PAD PD	AREA OCCUPATION RATE OF WIRING BENEATH BUMP	
	M1	M1
OUTSIDE OF GATE ZIGZAG	84.35%	94.00%
INSIDE OF GATE ZIGZAG	83.06%	92.18%
OUTSIDE OF SOURCE ZIGZAG	83.59%	93.91%
INSIDE OF SOURCE ZIGZAG	83.90%	94.48%
CORNER DUMMY	83.65%	94.77%
DIGITAL INPUT	83.66%	93.65%
DIGITAL INPUT (OSC2)	83.09%	94.05%
DUMMY AT INPUT SIDE	83.87%	94.81%
ANALOG INPUT 1	83.45%	94.81%
ANALOG INPUT 2	83.45%	94.81%
ANALOG INPUT 3	83.53%	94.81%
ANALOG INPUT 4	83.67%	94.81%
ANALOG INPUT 5	83.10%	94.81%
ANALOG INPUT 6	83.74%	94.62%
ANALOG INPUT 7	83.50%	94.81%
ANALOG INPUT 8	83.59%	94.81%
ANALOG INPUT 9	83.81%	94.81%

MINIMUM	83.06%	92.18%
MAXIMUM	84.35%	94.81%
DIFFERENCE BETWEEN MAXIMUM AND MINIMUM	1.29%	2.64%

AVERAGE	83.59%	94.41%
---------	--------	--------

FIG. 47

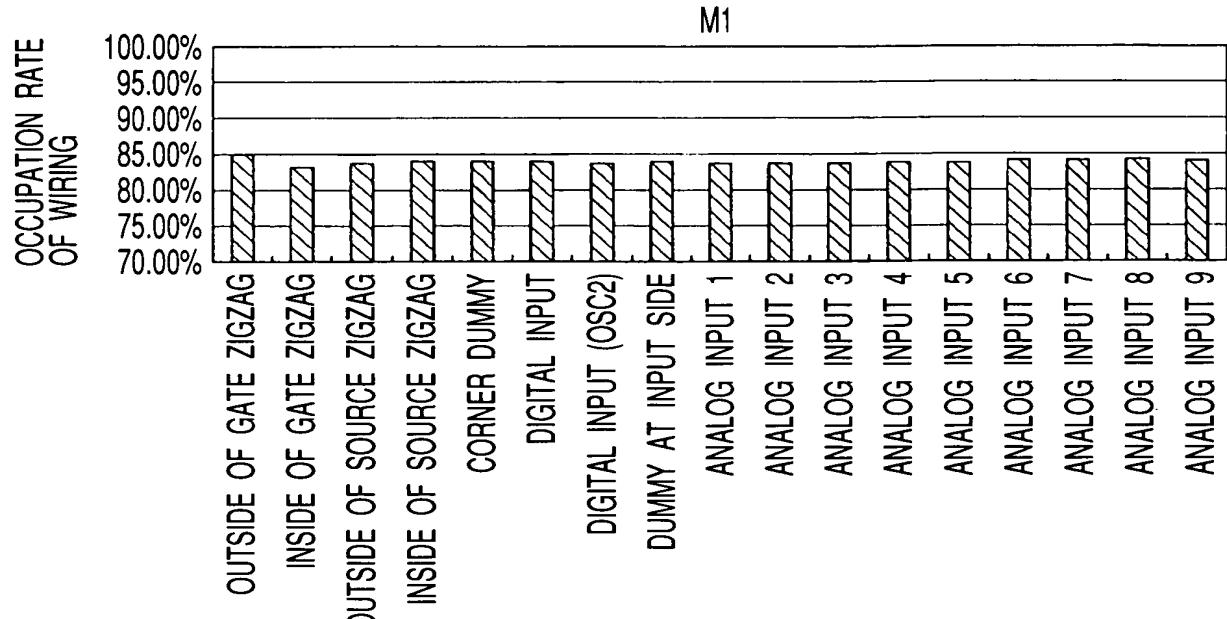
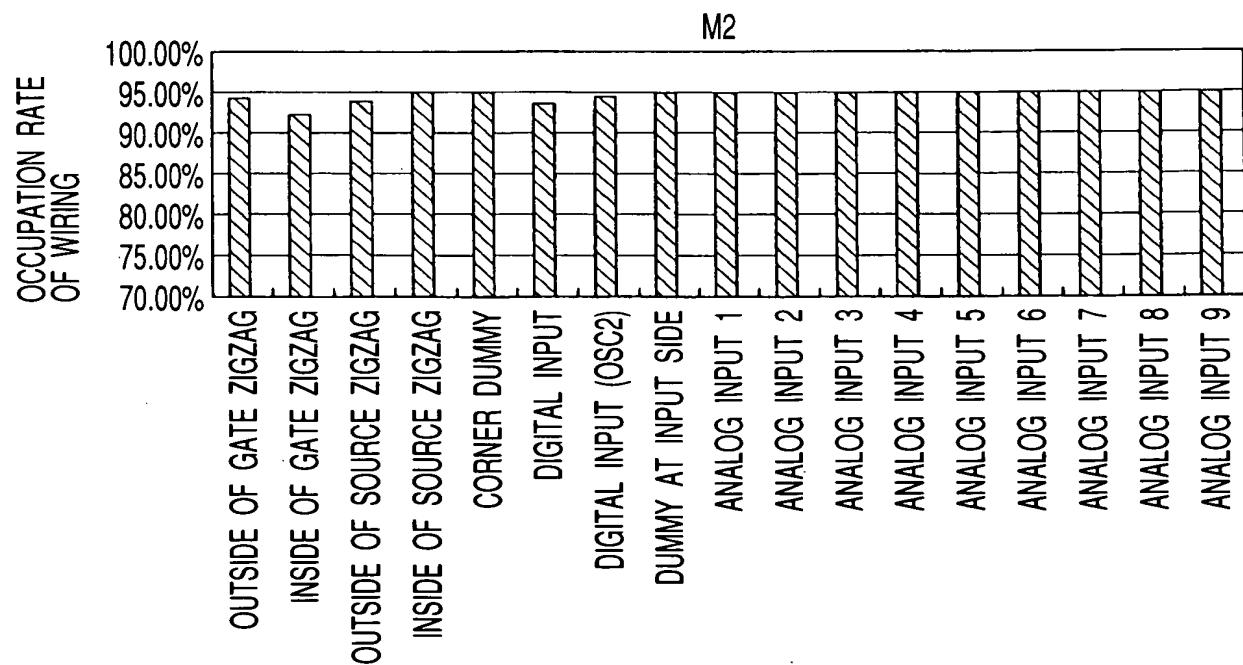
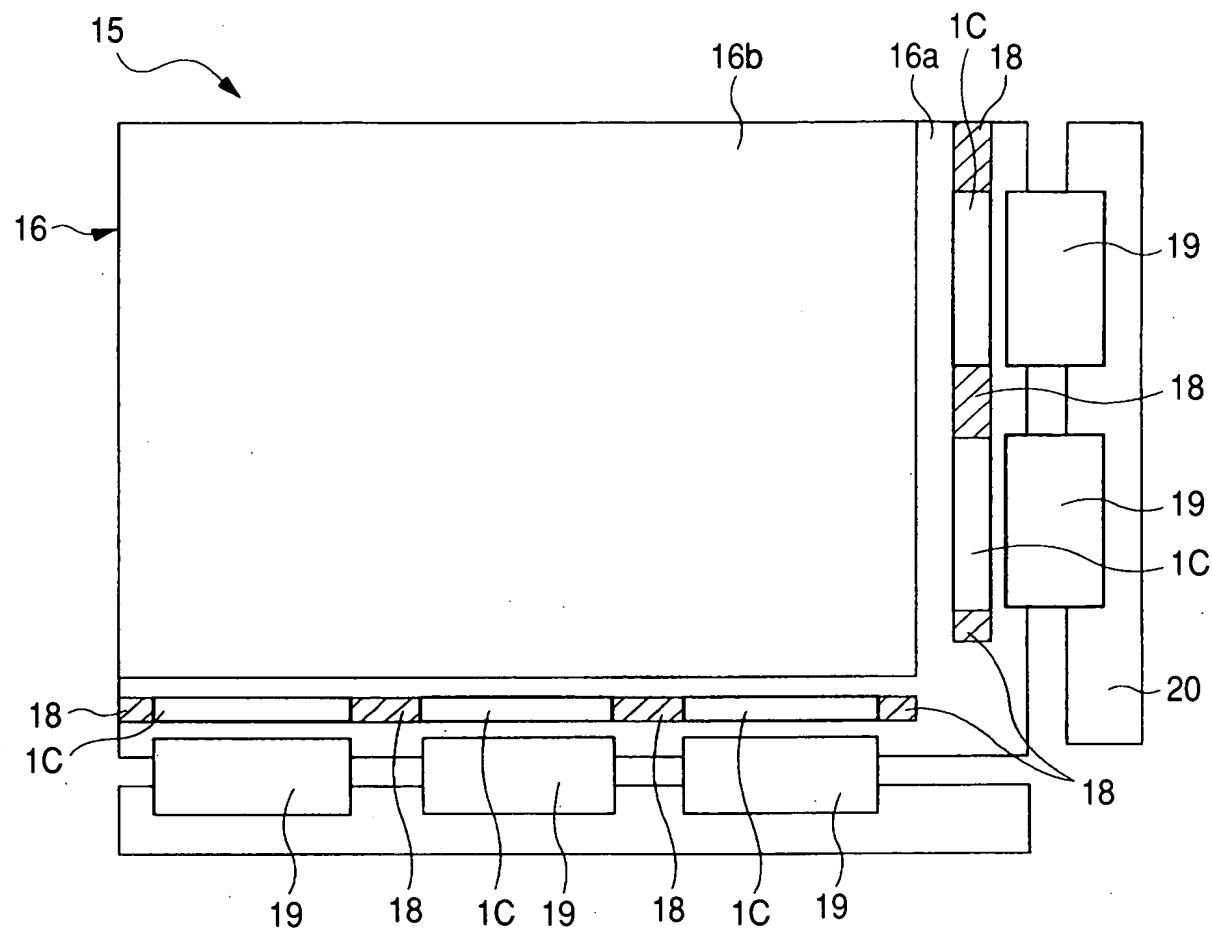
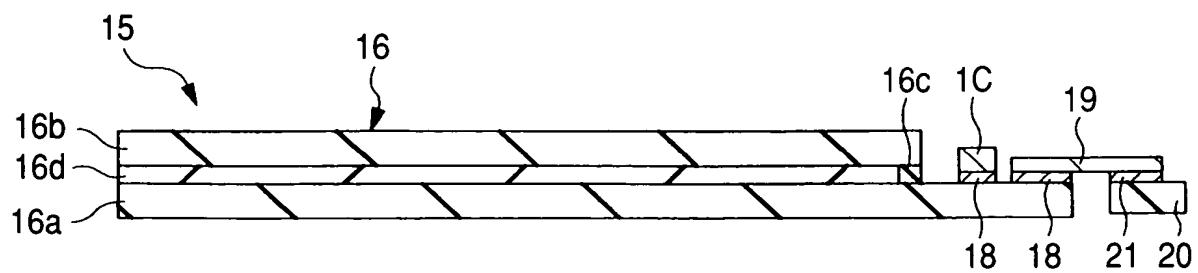
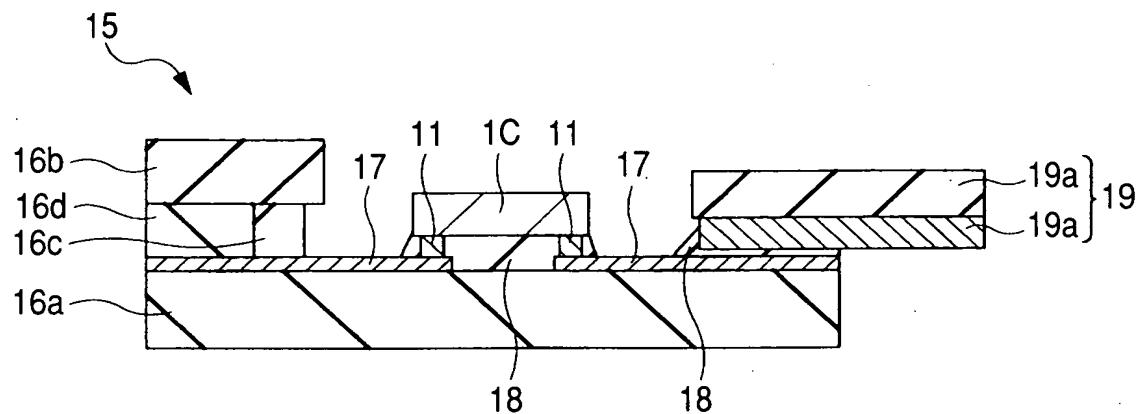


FIG. 48



**FIG. 49****FIG. 50**

**FIG. 51**



**FIG. 52**

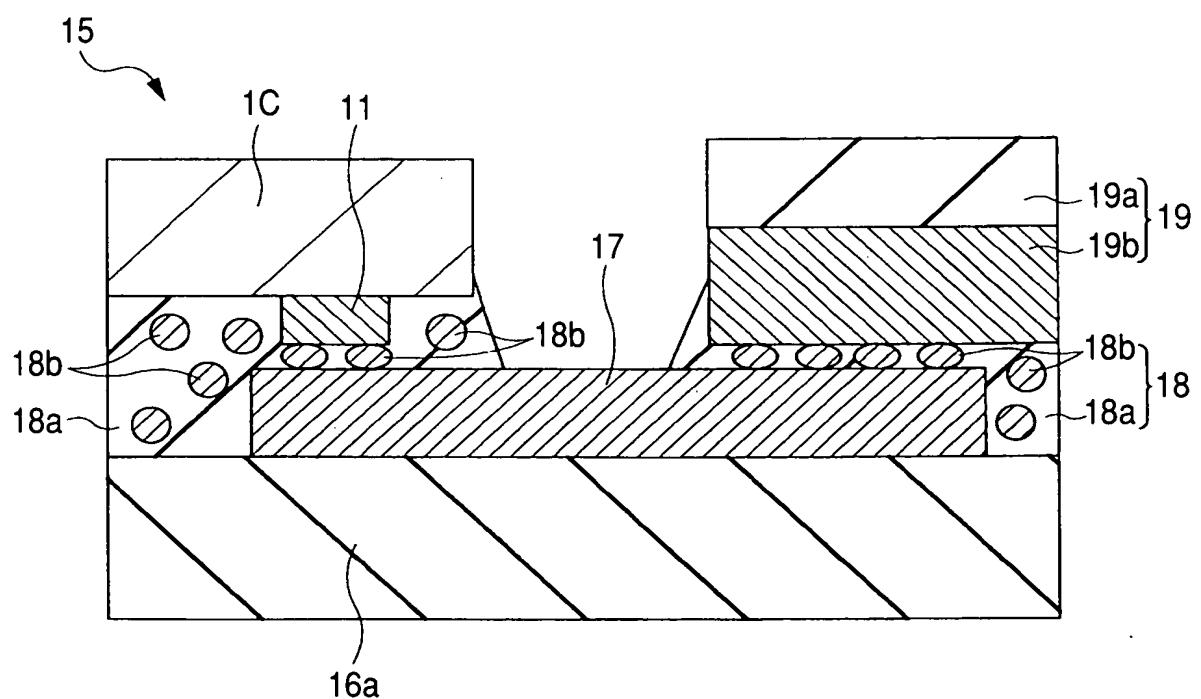


FIG. 53

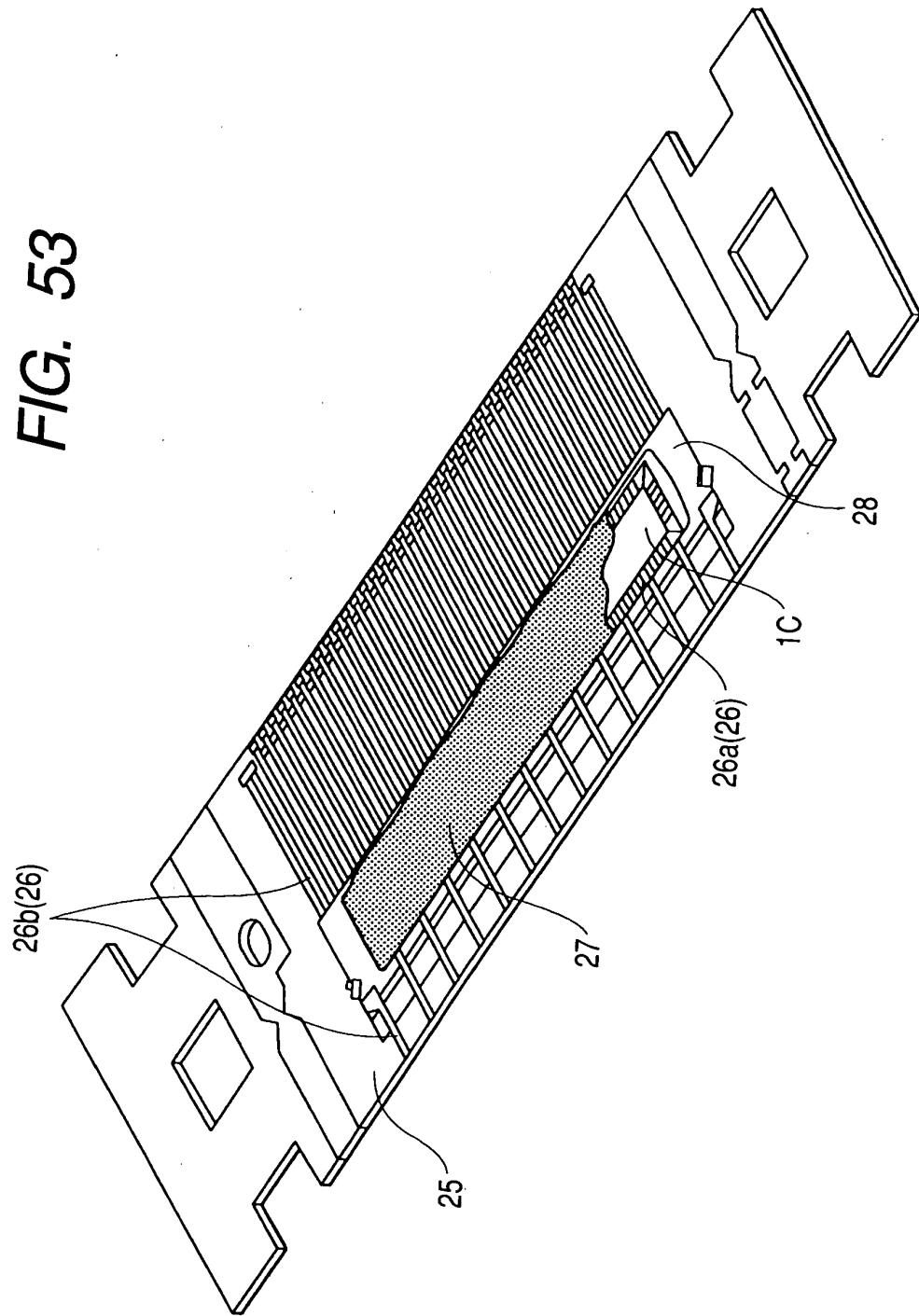


FIG. 54

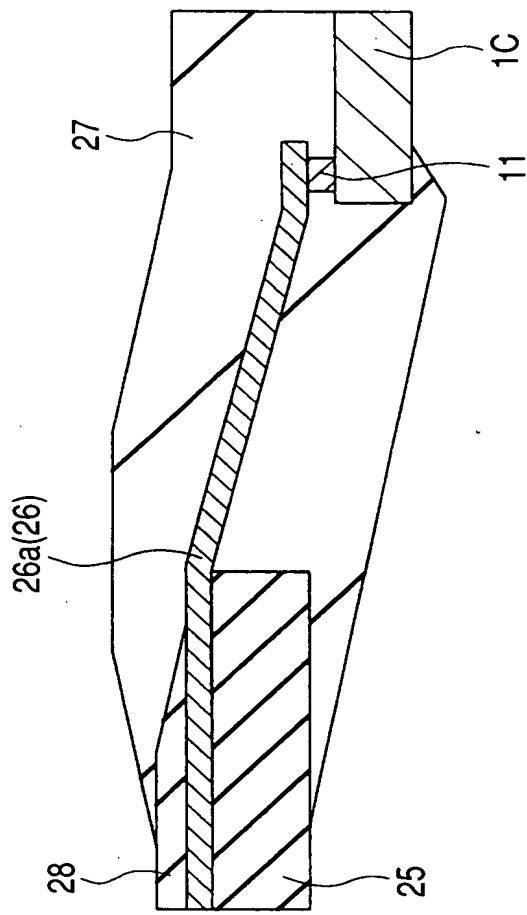


FIG. 55

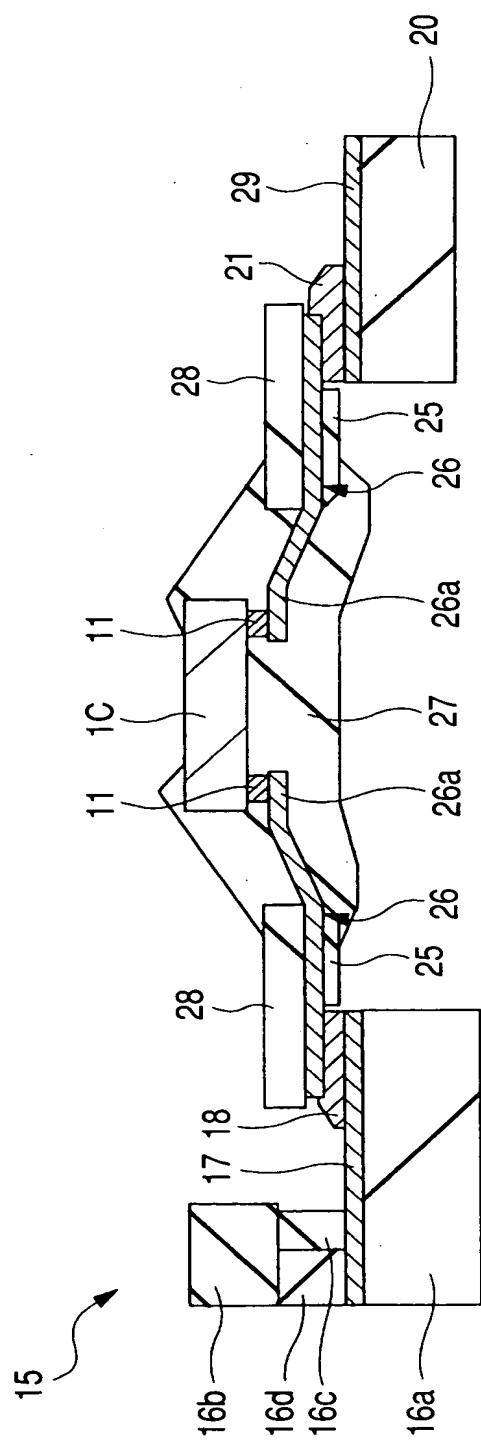


FIG. 56

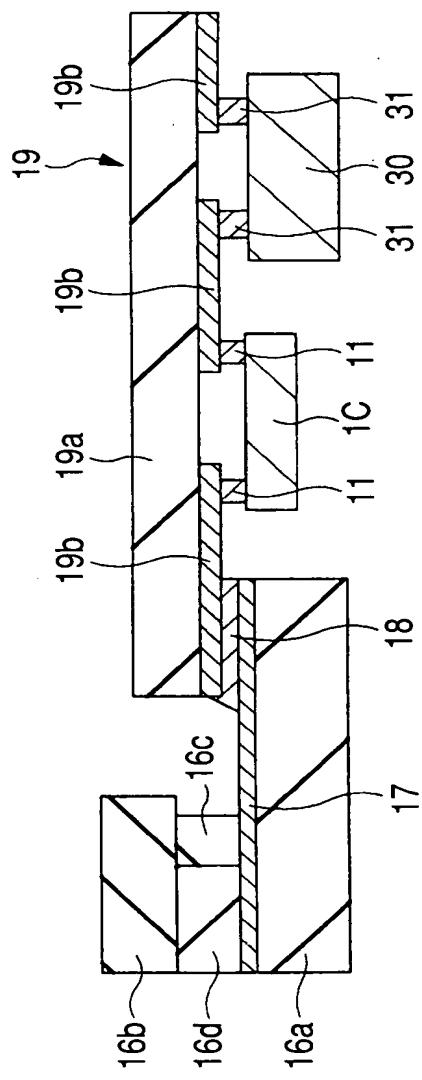


FIG. 57

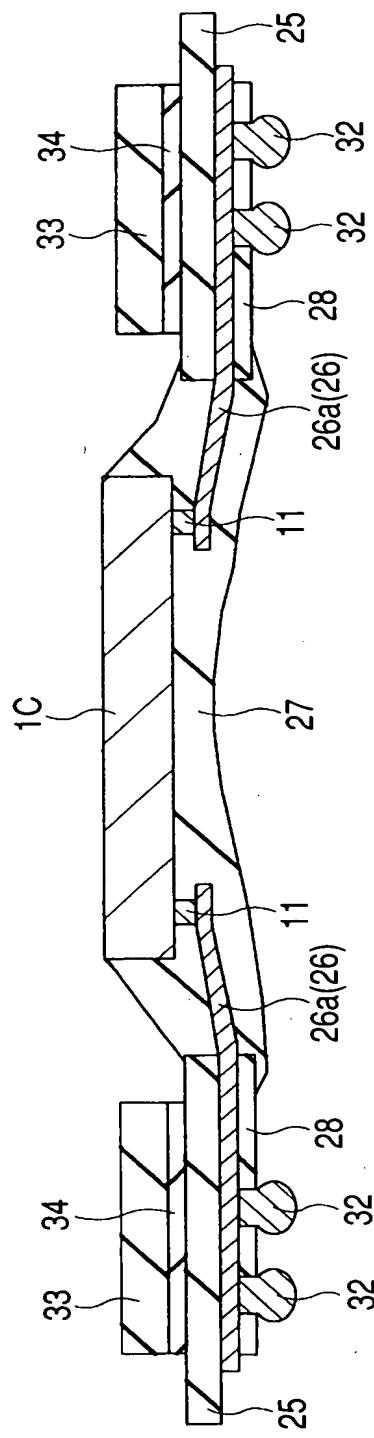
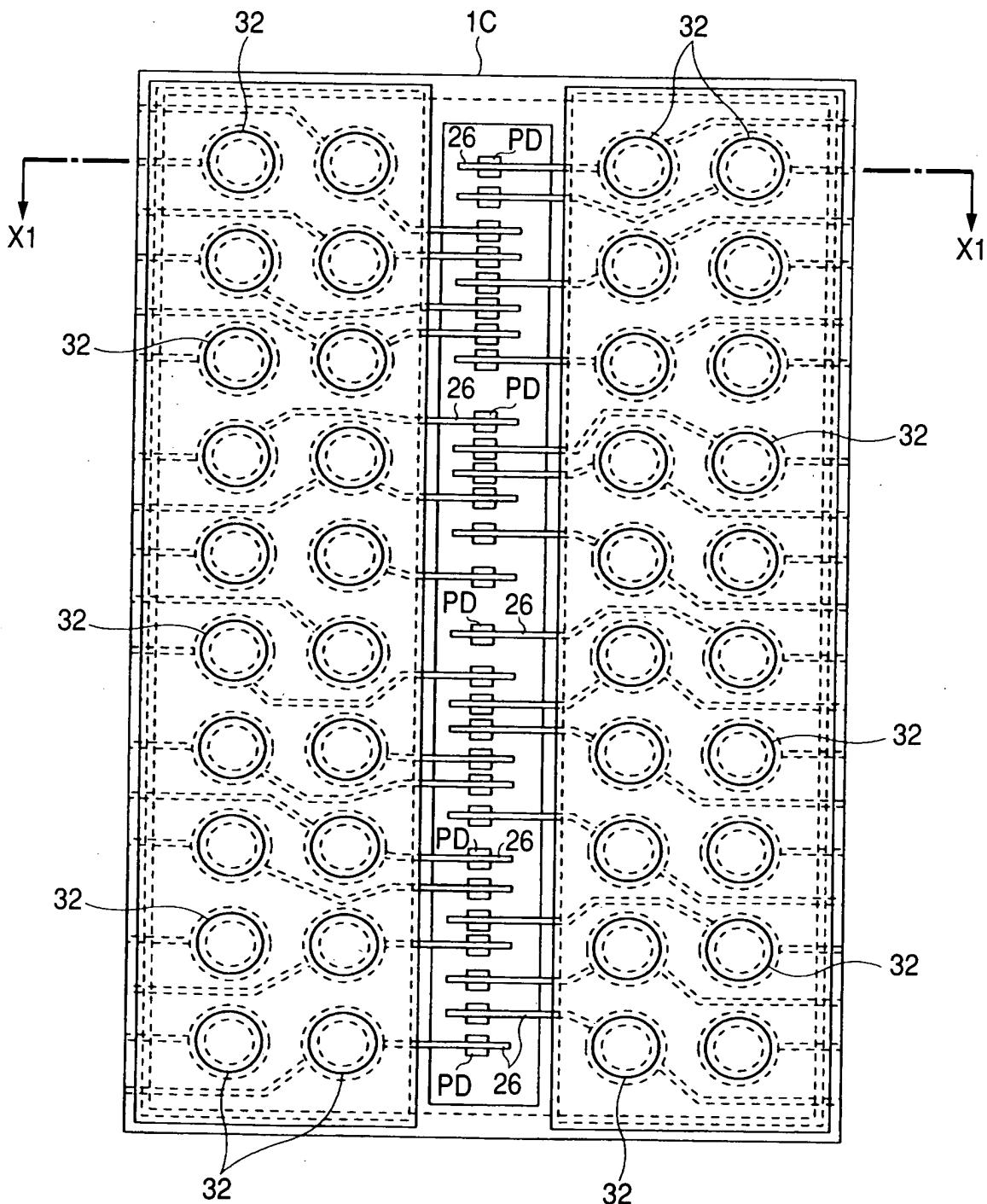
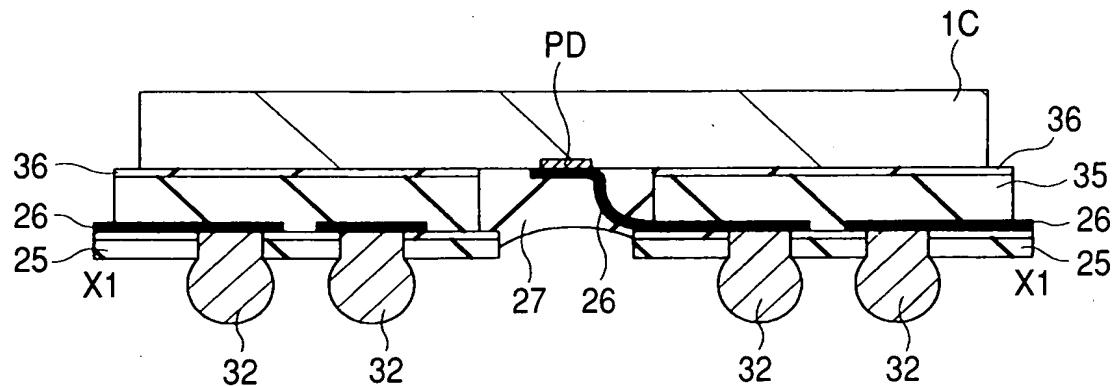


FIG. 58



**FIG. 59**



**FIG. 60**

